

Fig.1

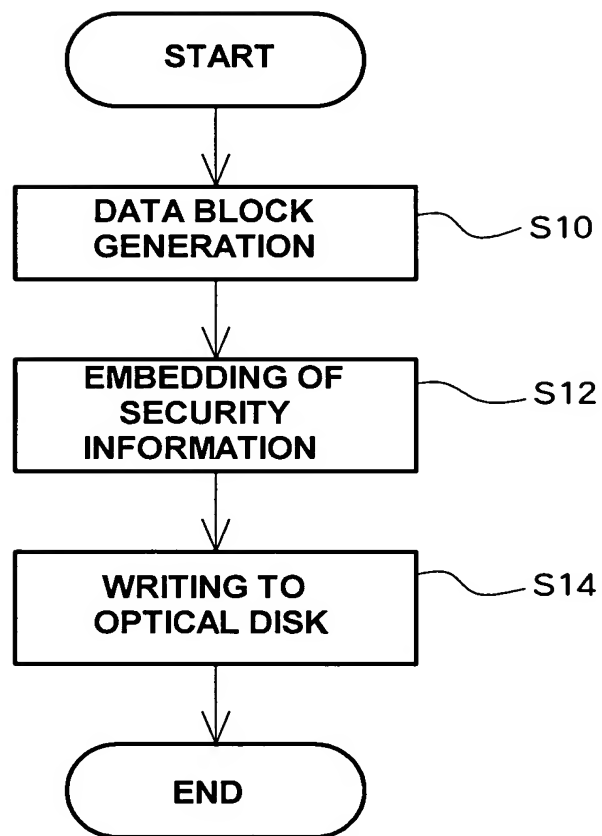


Fig. 2

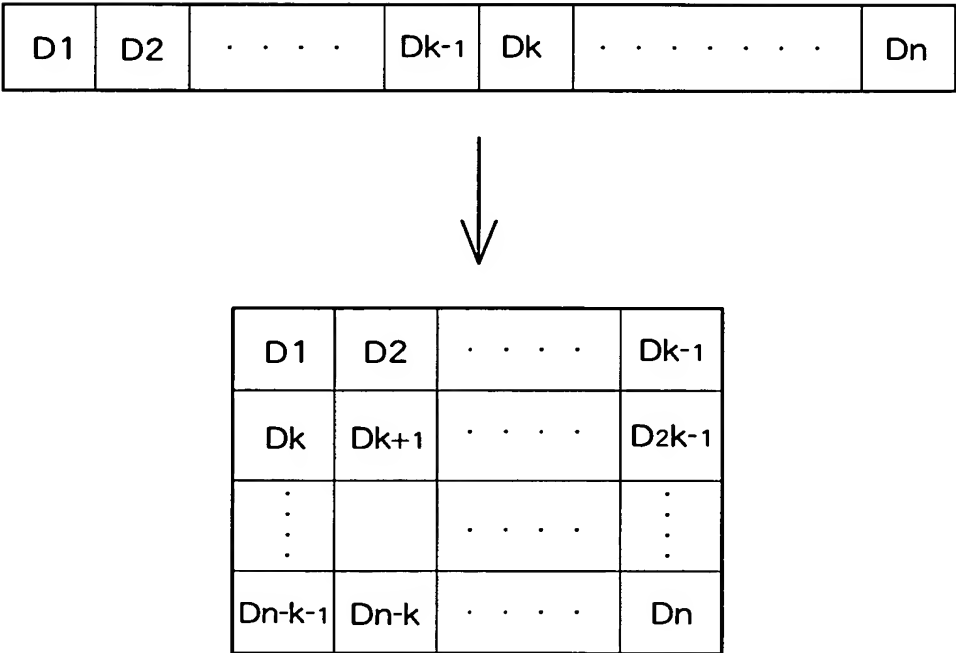


Fig. 3

Fig. 4

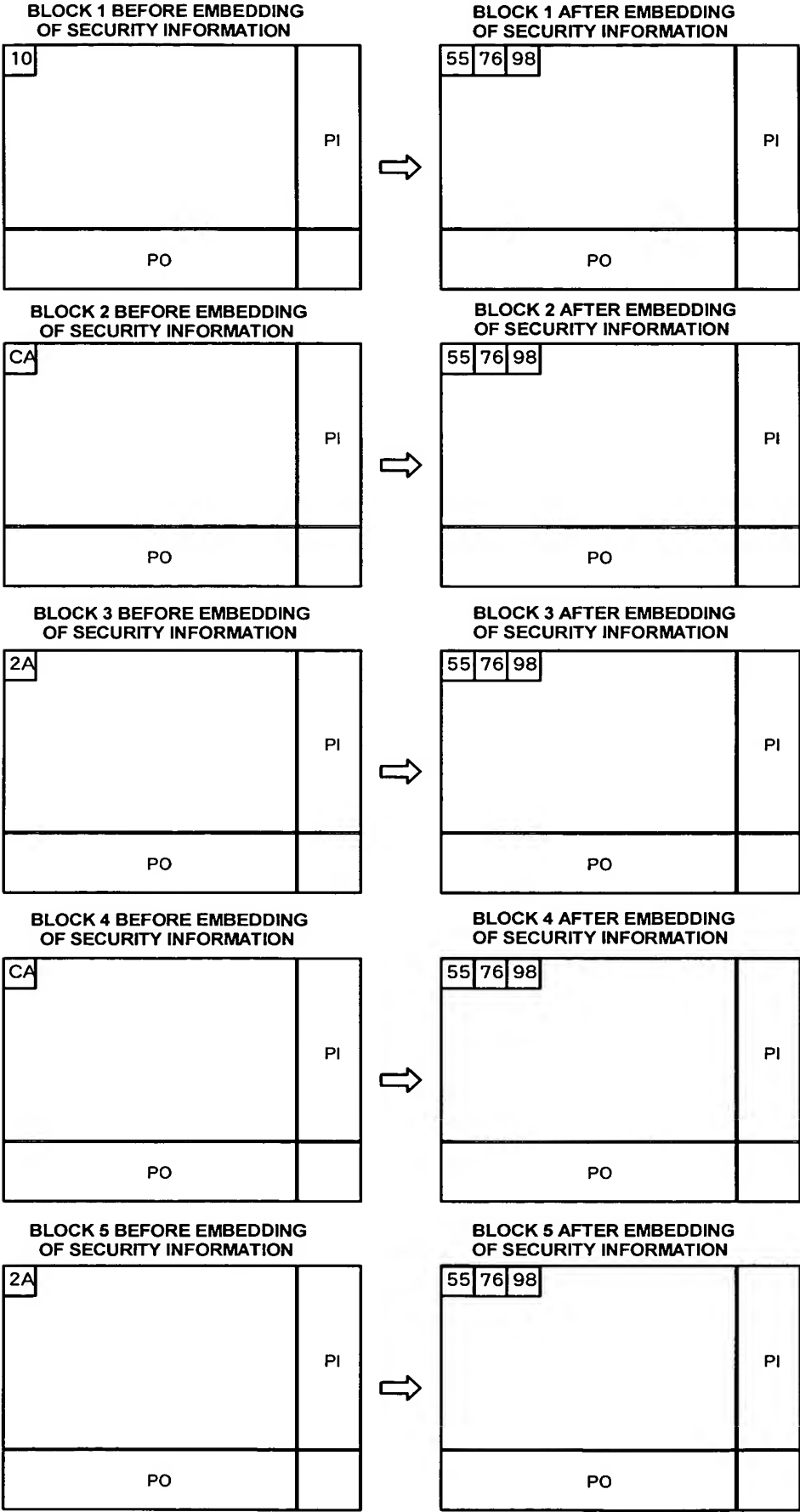


Fig. 5

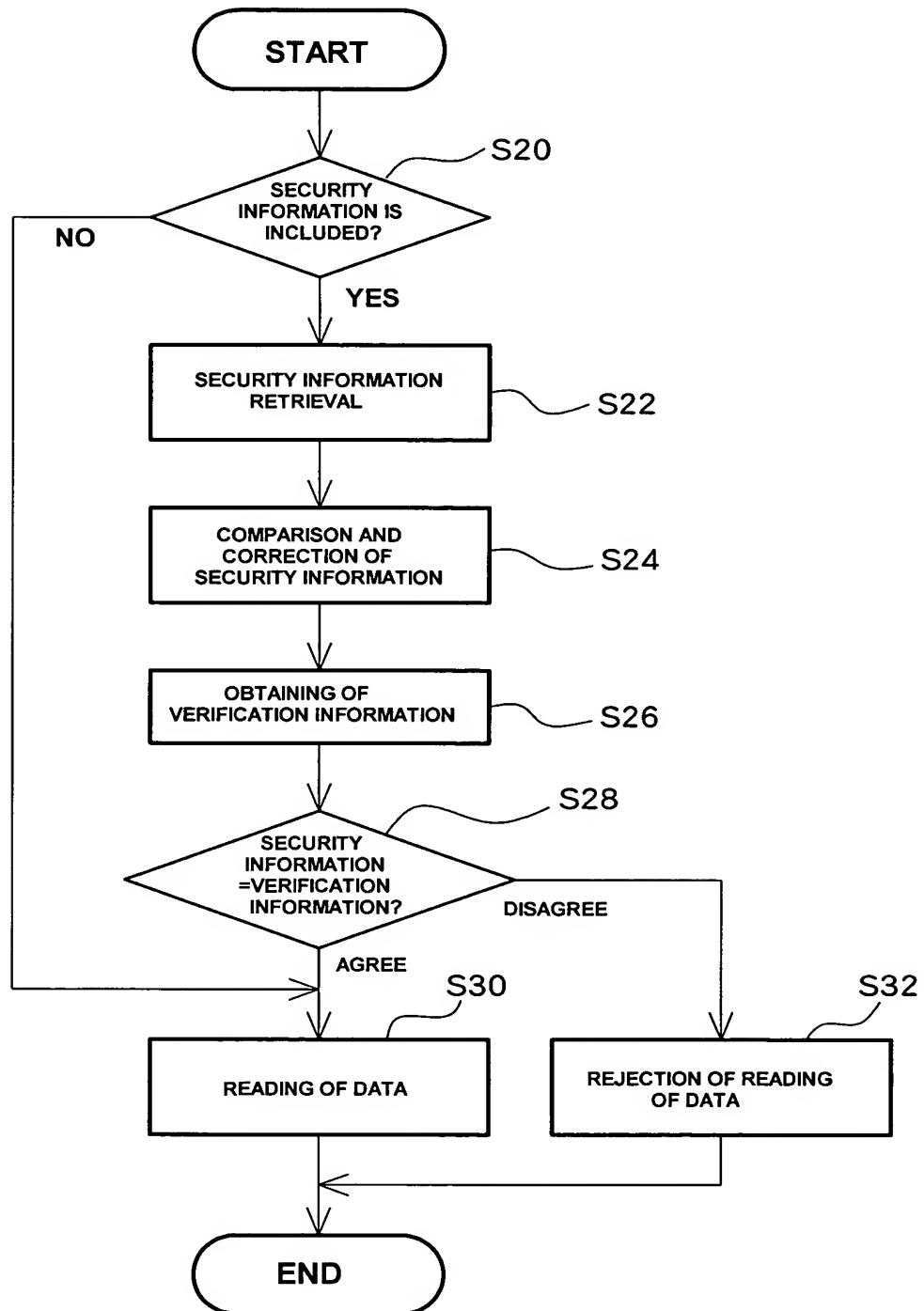


Fig. 6

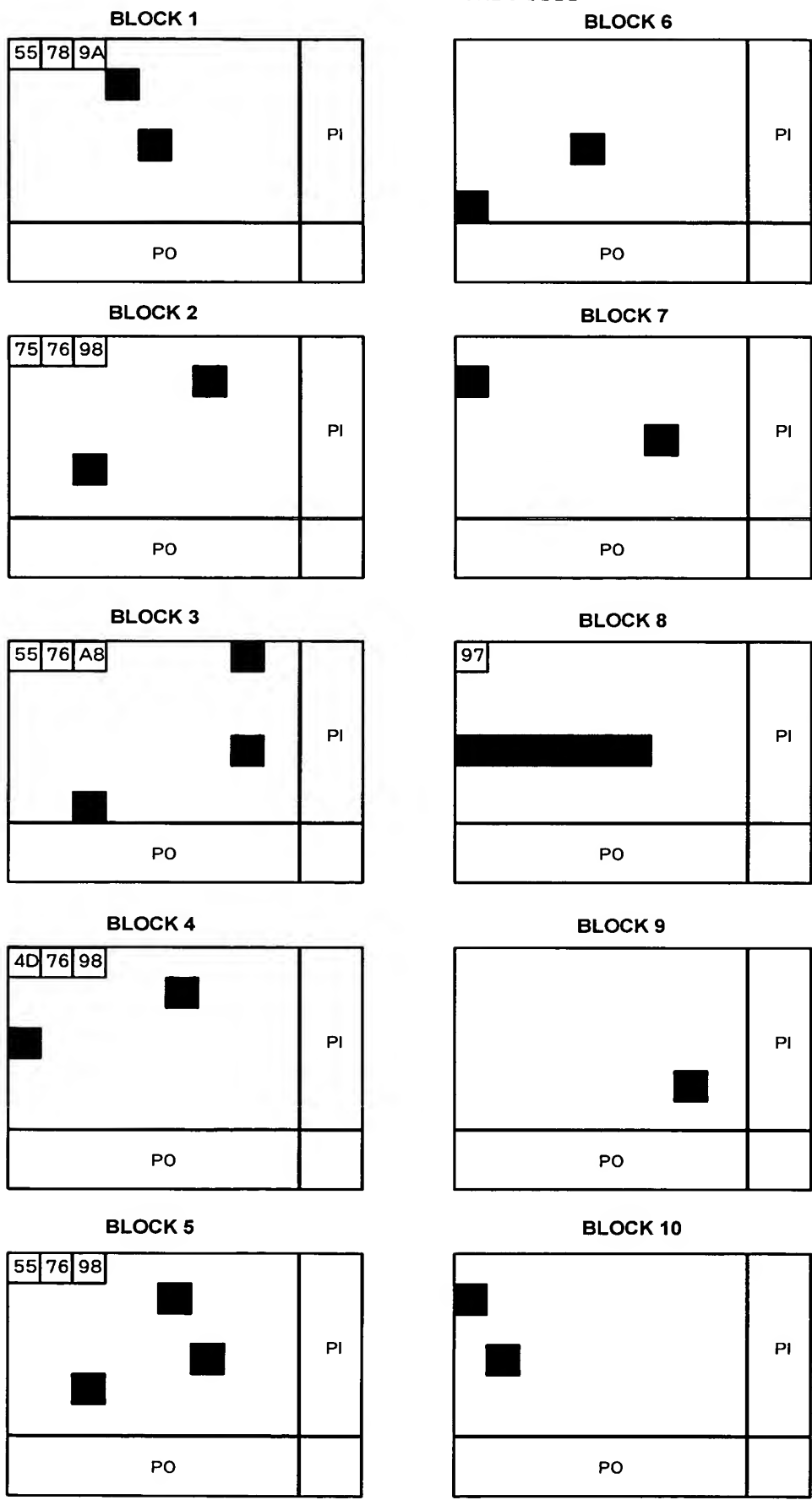


Fig. 7

		SECURITY INFORMATION BIT STRINGS							
		PRECEDING 4 BITS				FOLLOWING 4 BITS			
ECC DATA BLOCK 1	55h	0	1	0	1	0	1	0	1
ECC DATA BLOCK 2	75h	0	1	1	1	0	1	0	1
ECC DATA BLOCK 3	55h	0	1	0	1	0	1	0	1
ECC DATA BLOCK 4	4Dh	0	1	0	0	1	1	0	1
ECC DATA BLOCK 5	55h	0	1	0	1	0	1	0	1
ECC DATA BLOCK 8	97h	1	0	0	1	0	1	1	1
CORRECTED SECURITY INFORMATION	55h	0	1	0	1	0	1	0	1

Fig. 8